

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Tetsuya FUKUOKA et al.

Appln. No.: 10/806,417

Group Art Unit: 2827

Filed: March 23, 2004

Examiner: S. Mai

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

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SUPPLEMENTAL AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Supplementing the Amendment dated June 8, 2006, please further amend the above-identified patent application as indicated below.

Amendments to the claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 6 of this paper.